- 12-04 PTO/SB/21 (09-04) Approved for use through 07/31/2006. OMB 0651-0031 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE duction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. Application Number RATE ANSMITTAL 10/655.997 Filing Date September 5, 2003 First Named Inventor **FORM** Jon P. Daley Art Unit 2812 **Examiner Name** Unknown (to be used for all correspondence after initial filing) Attorney Docket Number M122-2280

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November __

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Patent Application Serial No	10/655.997
Filing Date	
Inventor	
Assignee	•
Group Art Unit	——————————————————————————————————————
Examiner	
Attorney Docket No	MI22-2380
Customer No	021567
Title: Methods of Forming Patterned Pho Substrates	otoresist Layers over Semiconductor

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to 1276 OG 55, August 5, 2003, no copies of cited U.S. patents or U.S. patent application publications are included, as the date of filing of this patent application occurs after June 30, 2003. A copy of the cited reference is attached. No admission is made regarding whether all the listed references are prior art.

Citation of the referenced art is respectfully requested.

This Supplemental Information Disclosure Statement is being filed before the mailing date of a first Office Action. There, no fee is believed to be required. However, in the event that a fee is required for filing this Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: //-9-04

Mark S. Matkin

Reg. No. 32,268

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